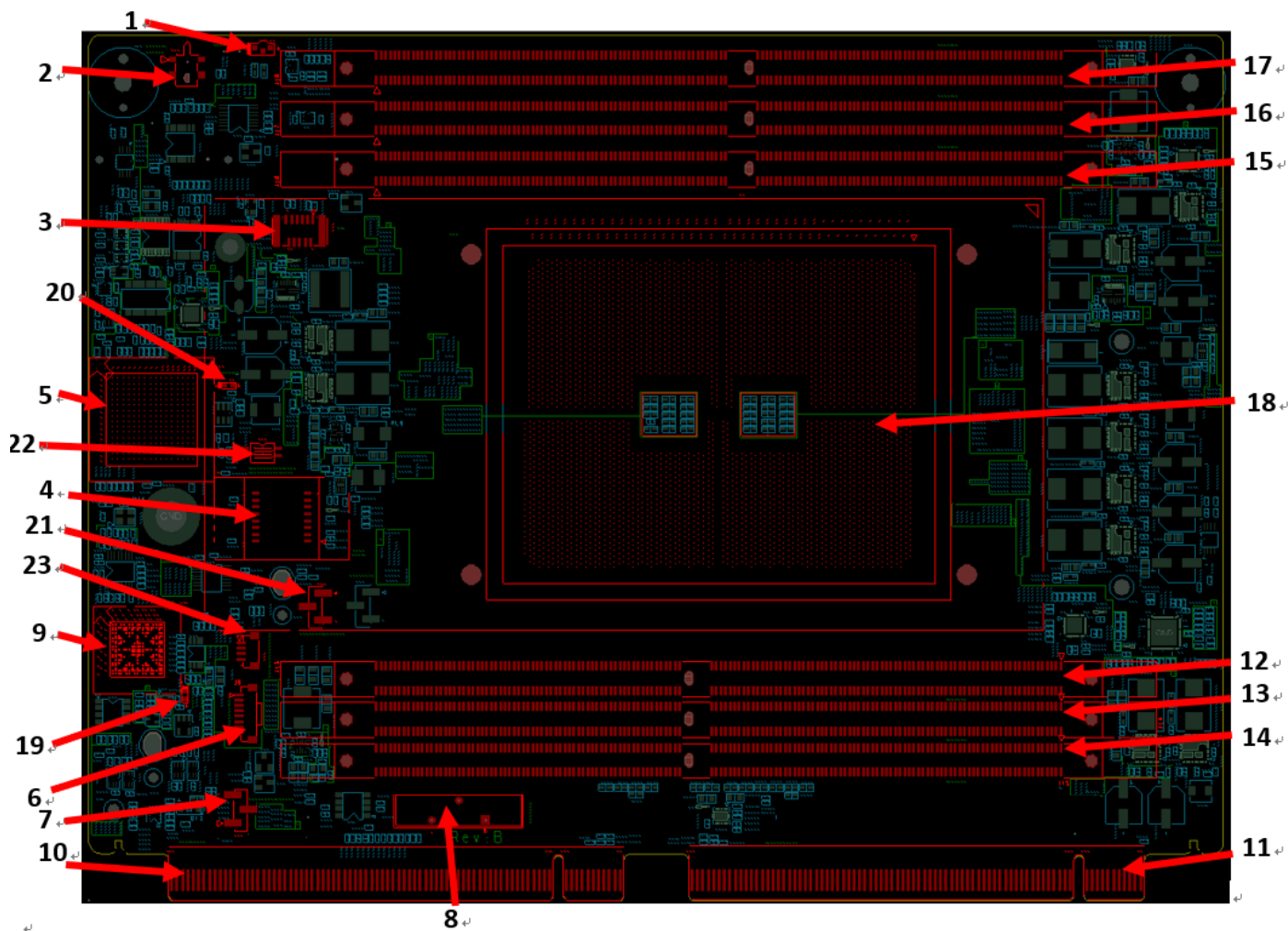
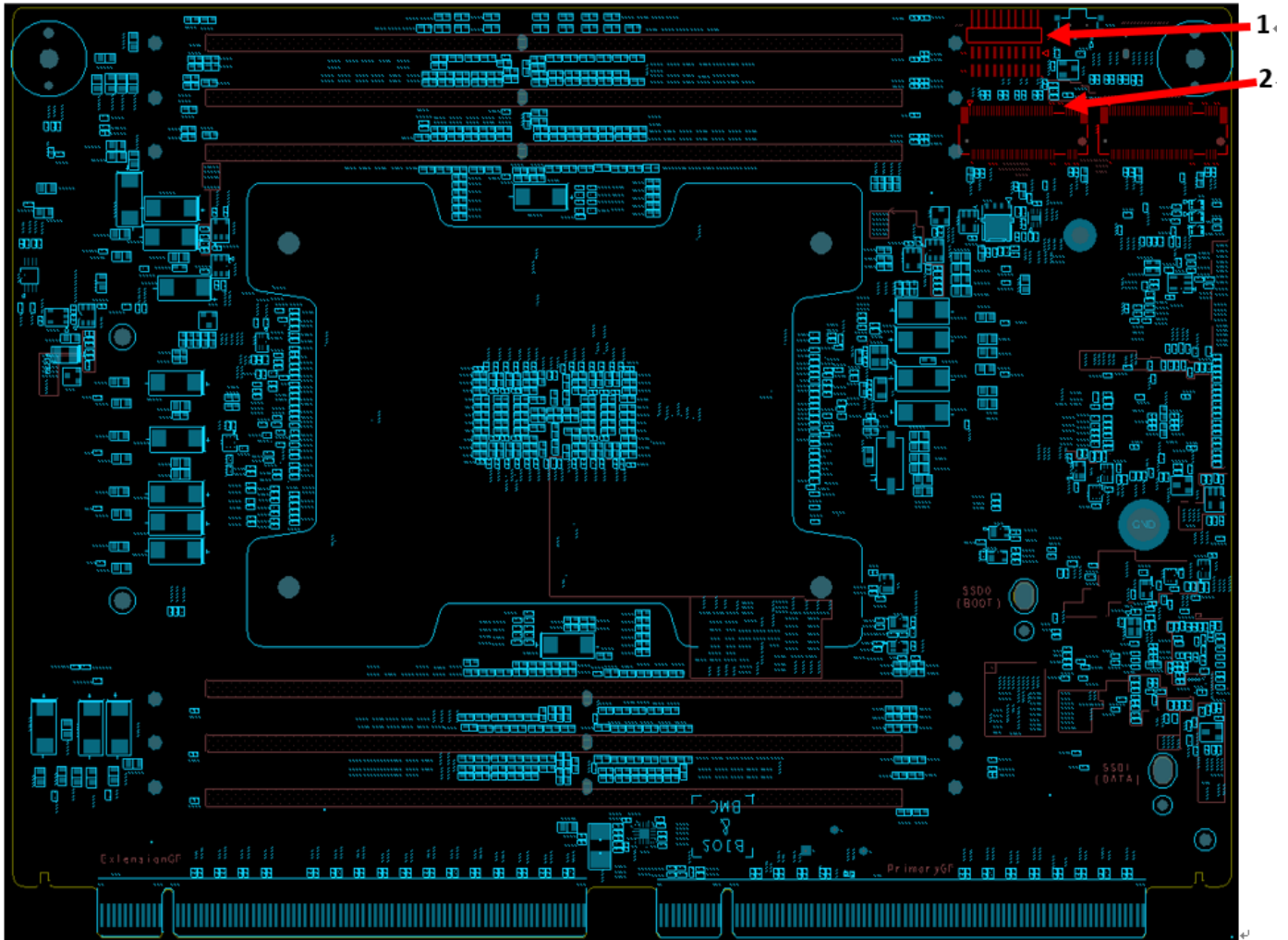




Item	Ref Designator / function	Description	Note
1	D4- Status LED	Power Led will light on when power on.	It is indicated P12V_STBY is ready.
2	SW1- Ejector switch	It will open when ejector release.	
3	J12-TPM	For TPM module.	
4	J7-SPI flash	1 st BIOS SPI flash	
5	U23-CPLD	CPLD chip	
6	J8- CPLD programming	CPLD in system programming header	
7	JP5- BIC Debug UART	Bridge IC debug connector	
8	BT1-Battery conn	Coin battery connector	
9	U26-Bridge IC	Bridge IC	
10	J45-Primary gold finger	Primary gold finger	
11	J46-Extension gold finger	Extension gold finger	
12	J13-DIMM_A0	DDR4 DIMM connector channel A DIMM0	
13	J14-DIMM_C0	DDR4 DIMM connector channel C DIMM0	
14	J15-DIMM_D0	DDR4 DIMM connector channel D DIMM0	
15	J16-DIMM_E0	DDR4 DIMM connector channel E DIMM0	
16	J17-DIMM_G0	DDR4 DIMM connector channel G DIMM0	
17	J18-DIMM_H0	DDR4 DIMM connector channel H DIMM0	
18	U1-CPU	CPU	
19	D2- BIC LED	BIC HEARTBEAT LED	
20	D1- CPLD LED	CPLD Debug LED	
21	JP4-Clear CMOS Jumper	Clear CMOS Jumper: pin 1-2 Open:Normal(default) Short:Clear CMOS	
22	SW4-BIC disable/CPLD debug mode switch	BIC disable/CPLD debug mode switch	
23	J44- BIC Debug JTAG	BIC JTAG	

Figure 1 North Dome board Top overview



Item	Ref Designator @PCB	Description	Note
1	J3-HDT	AMD HDT debug header	
2	J48-SSD0_BOOT	Boot Drive connector.	PCIe interface from CPU

Figure 2 North Dome board bottom overview